ATI FireProTM RG220

Wake-On-Lan Configuration & & PCoIP Reset

Revision 1.2



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Revision History

Revision	Date	Comments
1.0	29-Oct-10	Initial release by Paul Janes
1.1	19-Nov-10	Clarification on pins 2-3 for PCIE_WAKE#
1.2	17-Dec-10	Changes to legal disclaimer on the last page

Application Note - FirePro RG220 WoL Config & PCoIP Reset

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1. Overview

The purpose of this application note is to provide detailed steps for configuring and validating the Wake-On-Lan (WOL) capabilities and completing a full reset of the PCoIP settings to factory defaults for the ATI FirePro RG220.

This document assumes that the FirePro RG220 is configured and can successfully connect to a PCoIP Zero Client.

2. Requirements and Configuration

2.1 System Requirements

- PCI-Express host system with 1 available x16 slot
- Windows XP/VISTA/7 or Linux
- o ATI FirePro RG220
- o Driver version 8.703 or later
- WOL power cable¹ (Optional)
- o PCoIP Zero Client

2.2 System Configuration

There are 2 options for configuring WOL on FirePro RG220:

- 1. PCIE_WAKE# method
 - System must support power on by PCIE devices and may require the option to be explicitly enabled in the BIOS setup.
- 2. PWR_SW method
 - Requires an optional power cable which connects from the FirePro RG220 to PWR_SW pins on the motherboard

Note: The default position of the jumper is set to pins 2-3 for PCIE_WAKE#. If the jumper is not present on your board, then obtain one from an old hard drive or local PC shop.

2.2.1 PCIE_WAKE# Method

- 1. locate JU3300 and set the jumper position to pins 2-3 (default)
- 2. If necessary, set the system BIOS to power on by PCIE devices²

2.2.2 PWR_SW Method

- 1. locate JU3300 and set the jumper position to pins 1-2
- 2. Connect WOL power cable to JU3301(Figure1) to PWR_SW pins on the motherboard header³



¹ WOL power cable not included with retail product. Ordering P/N is 6110030500G.

² Refer to system documentation or contact manufacturer for further details

³ Refer to motherboard documentation to identify the PWR_SW pins

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3. Test Cases

3.1 Validate PCIE_Wake# and PWR_SW

The following test procedure applies to both methods

- 1. Boot host system normally and log in via PCoIP Zero Client
- 2. Put the host system into sleep mode
- 3. Push the Power/Standby button on Zero Client to send WOL magic packet
- 4. Wake from sleep should be initiated on the host system

4. PCoIP Reset

4.1 Description

If the FirePro RG220 web portal password is forgotten or lost, then a full reset of the PCoIP settings will be required.

4.2 Steps

- 1. Shut down PC and remove FirePro RG220
- 2. Locate JU3200(Figure2) and short pins with a jumper
- 3. Boot the system and allow OS to complete loading
- 4. Once complete shutdown the system and remove the jumper
- 5. Boot system again to complete the reset



Figure2

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